IN THE UNITED STATES PATENT AND TRADEMARK OFFICE Application of Yamazaki et al. I hereby certify that this correspondence is being Serial No.: 09/544,801 deposited with the United States Postal Service as first class mail in an envelope addressed to: the Assistant Commissioner for Patents, Washington Filed: April 7, 2000 (Date of Deposit Semiconductor Device And For: Method For Fabricating The Same Art Unit: 2826 Examiner: K. Quinto Commissioner for Patents Washington, D.C. 20231 December 19, 2002 **ELECTION AND AMENDMENT A** 

Sir:

In response to the Restriction Requirement of November 19, 2002, Applicants elect to prosecute Group I: Claims 1-27.

Applicants are making this election without prejudice to later filing a divisional application on the non-elected claims.

Please also amend the above-identified application as follows:

## IN THE CLAIMS:

Please amend the claims as follows:

3.(Amended) A semiconductor device according to claim 1, wherein said first conductive layer comprises at least one selected from Ta, W, Ti and Mo, and said second conductive layer comprises Al or Cu.

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